

1 1. (Amended) A method comprising:

2 extracting parameters of a set of domino logic circuits, each domino logic circuit of the
3 set of domino logic circuits having inputs and an output;

4 simulating each domino logic circuit of the set of domino logic circuits, each domino
5 logic circuit simulated after any domino logic circuit feeding into at least one of the inputs of the
6 domino logic circuit has been simulated; and

7 reporting results of the simulating.

1 3. (Amended) The method of claim 1 wherein:

2 simulating each domino logic circuit includes using the simulated results of circuits
3 coupled to the inputs of the domino logic circuit.

1 4. (Amended) A method, comprising:

2 scheduling a set of domino logic circuits into an ordered list, the ordered list positioning
3 all domino logic circuits of the set of domino logic circuits feeding into an input of another
4 domino logic circuit of the set of domino logic circuits before a position of the another domino
5 logic circuit in the ordered list; and

6 simulating each domino logic circuit according to the ordered list.

1 5. The method of claim 4 further comprising:

2 extracting the parameters for each domino logic circuit of the set of domino logic circuits.

1 6. The method of claim 5 further comprising:
2 reporting results of the simulating.

1 8. (Amended) The method of claim 6 wherein:
2 the extracting further including extracting parameters of non-domino circuits;
3 the scheduling further including scheduling non-domino circuits into the ordered list; and
4 the simulating further including simulating non-domino circuits.

1 9. The method of claim 8 wherein:
the reporting further including reporting results of the simulating non-domino circuits.

1 10. (Amended) A machine readable medium embodying instructions which, when
2 executed by a processor, cause the processor to perform a method, the method comprising:
3 scheduling a set of domino logic circuits into an ordered list, the ordered list positioning
4 all domino logic circuits of the set of domino logic circuits feeding into an input of another
5 domino logic circuit of the set of domino logic circuits before a position of the another domino
6 logic circuit in the ordered list; and
7 simulating each domino logic circuit according to the ordered list.

1 11. The machine readable medium of claim 10 further embodying instructions which,
2 when executed by a processor, cause the processor to perform the method further comprising:
3 extracting the parameters for each domino logic circuit of the set of domino logic circuits.

1 12. The machine readable medium of claim 11 further embodying instructions which,
2 when executed by a processor, cause the processor to perform the method further comprising:
3 reporting results of the simulating.

1 14. (Amended) The machine readable medium of claim 12 further embodying
2 instructions which, when executed by a processor, cause the processor to perform the method
3 wherein:
4 the extracting further including extracting parameters of non-domino circuits;
5 the scheduling further including scheduling non-domino circuits into the ordered list; and
6 the simulating further including simulating non-domino circuits.

1 15. (Amended) A system comprising:
2 a processor;
3 a memory controller coupled to the processor;
4 a memory coupled to the memory controller;
5 wherein the processor executes instructions to perform the method of:
6 scheduling a set of domino logic circuits into an ordered list, the ordered list positioning
7 all domino logic circuits of the set of domino logic circuits feeding into an input of another
8 domino logic circuit of the set of domino logic circuits before a position of the another domino
9 logic circuit in the ordered list; and
10 simulating each domino logic circuit according to the ordered list.

1 16. The system of claim 15 wherein the processor further executes instructions to perform
2 the method further comprising:
3 extracting the parameters for each domino logic circuit of the set of domino logic circuits;
4 and
5 reporting results of the simulating.

1 17. (Amended) An apparatus comprising:
2 means for extracting parameters for each domino logic circuit of a set of domino logic
3 circuits;
4 means for scheduling the set of domino logic circuits into an ordered list, the ordered list
5 positioning all domino logic circuits of the set of domino logic circuits feeding into an input of
6 another domino logic circuit of the set of domino logic circuits before a position of the another
7 domino logic circuit in the ordered list;
8 means for simulating each domino logic circuit according to the ordered list
9 means for reporting results of the means for simulating.

1 18. (New) The method of claim 3 wherein:
2 simulating each domino logic circuit includes generating output results of each domino
3 logic circuit, the output results including worst-case noise that will be generated by each domino
4 logic circuit.

1 19. (New) The method of claim 18 wherein:

2 reporting the results of the simulating includes indicating whether each domino logic
3 circuit is likely to generate an erroneous output.

1 20. (New) The method of claim 4 wherein:

2 simulating each domino logic circuit includes generating output results of each domino
3 logic circuit, the output results including worst-case noise that will be generated by each domino
4 logic circuit.

1 21. (New) The method of claim 20, further comprising:

2 reporting results of the simulating indicating whether each domino logic circuit is likely
3 to generate an erroneous output.

1 22. (New) The machine readable medium of claim 10, further embodying instructions

2 which, when executed by a processor, cause the processor to perform the method wherein:

3 simulating each domino logic circuit includes generating output results of each domino
4 logic circuit, the output results including worst-case noise that will be generated by each domino
5 logic circuit.

1 23. (New) The machine readable medium of claim 22, further embodying instructions
2 which, when executed by a processor, cause the processor to perform the method further
3 comprising:

4 reporting results of the simulating indicating whether each domino logic circuit is likely
5 to generate an erroneous output.

1 24. (New) The system of claim 15 wherein:

2 simulating each domino logic circuit includes generating output results of each domino
3 logic circuit, the output results including worst-case noise that will be generated by each domino
4 logic circuit.

1 25. (New) The system of claim 24, further comprising:

2 reporting results of the simulating indicating whether each domino logic circuit is likely
3 to generate an erroneous output.

1 26. (New) The system of claim 17 wherein:

2 means for simulating each domino logic circuit includes generating output results of each
3 domino logic circuit, the output results including worst-case noise that will be generated by each
4 domino logic circuit.

1 27. (New) The system of claim 26 wherein:

2 means for reporting the results of the means of simulating includes indicating whether

3 each domino logic circuit is likely to generate an erroneous output.
